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Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

On: February 15, 2005

By: Richard D. Fuerle

Signature: 

Date: February 15, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: YOSHIYUKI ANDO

Examiner: Miller, Craig S.

Serial No.: 10/707,450

Group Art Unit: 2857

Filed: December 15, 2003

For: INTEGRATED CIRCUITS HAVING  
POST-SILICON ADJUSTMENT CONTROL

The Commissioner of Patents  
and Trademarks  
Alexandria, VA 22313

**AMENDMENT**

Introductory Comments

In response to the Office Action dated December 14, 2004, please amend  
the above-identified patent application as follows: